

10/648,886

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 10/648,886
Confirmation No. 4873
Filing Date August 26, 2003
Inventor H. Montgomery Manning
Assignee Micron Technology, Inc.
Group Art Unit 2811
Examiner O. Nadav
Attorney's Docket No. MI22-2374
Customer Number 021567
Title: Integrated Circuitry and a Semiconductor Processing Method of Forming a
Series of Conductive Lines

RESPONSE TO APRIL 28, 2004 OFFICE ACTION

To: Commissioner for Patents
P.O. Box 1450
Art Unit 2811
Alexandria, VA 22313-1450

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AMENDMENTS

MAY 20 2004

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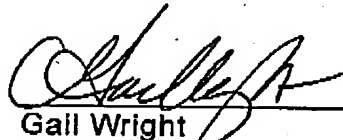
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Dated:

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By:



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TRANSMITTAL FORM <i>(to be used for all correspondence after initial filing)</i>	Application Number	10/648,886
	Filing Date	August 26, 2003
	First Named Inventor	Montgomery Manning
	Art Unit	2811
	Examiner Name	O. Nadav
Total Number of Pages In This Submission	Attorney Docket Number	MI22-2374

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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT		
Firm or Individual	James E. Lako Reg. No. 44,854, Wells St. John P.S.	
Signature	<i>[Signature]</i>	
Date	5/19/04	

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